



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Teruyuki Maeda

U.S.S.N.:

10/735,399

EXAMINER: Not yet assigned

FILED:

December 11, 2003

GROUP:

2811

FOR:

POWER TRANSISTOR AND SEMICONDUCTOR INTEGRATED CIRCUIT

USING THE SAME

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

CERTIFICATION UNDER 37 C.F.R. 1.10*

(Express Mail label number is mandatory.)
(Express Mail certification is optional.)

I hereby certify that this correspondence and the documents referred to as attached therein are being deposited with the United States Postal Service on this date <u>April 12, 2004</u> in an envelope as "Express Mail Post Office to Addressee," mailing Label Number <u>EV 438 971 726 US</u> addressed to the: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Elena Misiaszek

CHANGE OF ATTORNEY'S MAILING ADDRESS IN APPLICATION

Applicant requests that all further communications from the Patent and Trademark Office in connection with the subject application be sent to the following address:

Edwards & Angell, LLP
Intellectual Property Practice Group
P.O. Box 55874
Boston, MA 02205

Tel.: (617) 439-4444 Facsimile: (617) 439-4170

Respectfully submitted,

Date: April 12, 2004

Customer No.: 21874

By:

George N. Chaclas (Reg. No. 46,608)

EDWARDS & ANGELL, LLP

P.O. Box 55874

Boston, MA 02205

Telephone: 860-541-7720 Facsimile: 617-439-4170